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| **Data Sheet report: All values displayed in nanoseconds (ns)** |

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| **Setup/Hold to clock CLOCK** |
| |  |  |  |  |  |  |  | | --- | --- | --- | --- | --- | --- | --- | | **Source** | **Max Setup to clk (edge)** | **Process Corner** | **Max Hold to clk (edge)** | **Process Corner** | **Internal Clock(s)** | **Clock Phase** | | LIGHT\_IN | -3.156(R) | FAST | 3.739(R) | SLOW | CLOCK\_BUFGP | 0.000 | | MOIS\_IN<0> | -3.481(R) | FAST | 4.065(R) | SLOW | CLOCK\_BUFGP | 0.000 | | MOIS\_IN<1> | -3.288(R) | FAST | 3.871(R) | SLOW | CLOCK\_BUFGP | 0.000 | | MOIS\_IN<2> | -3.566(R) | FAST | 4.150(R) | SLOW | CLOCK\_BUFGP | 0.000 | | RESET | -3.443(R) | SLOW | 3.906(R) | FAST | CLOCK\_BUFGP | 0.000 | | TEMP\_IN | -3.135(R) | FAST | 3.719(R) | SLOW | CLOCK\_BUFGP | 0.000 | |

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| **Clock CLOCK to Pad** |
| |  |  |  |  |  |  |  | | --- | --- | --- | --- | --- | --- | --- | | **Destination** | **Max (slowest) clk (edge) to PAD** | **Process Corner** | **Min (fastest) clk (edge) to PAD** | **Process Corner** | **Internal Clock(s)** | **Clock Phase** | | FSTATE<0> | 8.171(R) | SLOW | 5.914(R) | FAST | CLOCK\_BUFGP | 0.000 | | SEG<1> | 8.323(R) | SLOW | 6.066(R) | FAST | CLOCK\_BUFGP | 0.000 | | SEG<2> | 8.320(R) | SLOW | 6.063(R) | FAST | CLOCK\_BUFGP | 0.000 | | SEG<4> | 8.286(R) | SLOW | 6.029(R) | FAST | CLOCK\_BUFGP | 0.000 | | SEG<5> | 8.166(R) | SLOW | 5.909(R) | FAST | CLOCK\_BUFGP | 0.000 | |

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| **Pad to Pad** |
| |  |  |  | | --- | --- | --- | | **Source Pad** | **Destination Pad** | **Delay** | | LIGHT\_IN | LIGHT\_OUT | 3.296 | | MOIS\_IN<0> | MOIS\_OUT<0> | 3.301 | | MOIS\_IN<1> | MOIS\_OUT<1> | 3.270 | | MOIS\_IN<2> | MOIS\_OUT<2> | 3.308 | | TEMP\_IN | TEMP\_OUT | 3.273 | |